

SEMICONDUCTOR

500 mW DO-35 Hermetically Sealed Glass Zener Voltage Regulators



Absolute Maximum Ratings T_A = 25°C unless otherwise noted

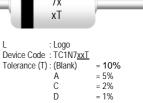
Parameter	Value	Units
Power Dissipation	500	mW
Storage Temperature Range	-65 to +200	°C
Operating Junction Temperature	+200	°C

These ratings are limiting values above which the serviceability of the diode may be impaired.

Specification Features:

- Zener Voltage Range 3.3 to 12 Volts
- DO-35 Package (JEDEC)
- Through-Hole Device Type Mounting
- Hermetically Sealed Glass
- Compression Bonded Construction
- All External Surfaces Are Corrosion Resistant And Leads Are Readily Solderable
- RoHS Compliant
- Solder Hot Dip Tin (Sn) Lead Finish
- Cathode Indicated By Polarity Band







ELECTRICAL SYMBOL

Electrical Characteristics $T_A = 25$ °C unless otherwise noted

Device Type	V _z @ I _{zT} (Volts) Nominal	I _{zT} (mA)	$\mathbf{Z}_{ZT} @ \mathbf{I}_{ZT} \ (\Omega) \ Max$	I _R @ V _R (μΑ) Max	V _R (Volt)
TC1N746A	3.3	20	28	10	1
TC1N747A	3.6	20	24	10	1
TC1N748A	3.9	20	23	10	1
TC1N749A	4.3	20	22	2	1
TC1N750A	4.7	20	19	2	1
TC1N751A	5.1	20	17	1	1
TC1N752A	5.6	20	11	1	1
TC1N753A	6.2	20	7	0.1	1
TC1N754A	6.8	20	5	0.1	1
TC1N755A	7.5	20	6	0.1	1
TC1N756A	8.2	20	8	0.1	1
TC1N757A	9.1	20	10	0.1	1
TC1N758A	10	20	17	0.1	1
TC1N759A	12	20	30	0.1	1

 V_F Forward Voltage = 1.5 V Maximum @ I_F = 200 mA for all types



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Notes:

1. TOLERANCE AND VOLTAGE DESIGNATION

The type numbers listed have zener voltage as shown and have a standard tolerance on the nominal zener voltage of $\pm 5\%$. Suffix (BLANK) = $\pm 10\%$, Suffix C = $\pm 2\%$ and D = $\pm 1\%$.

2. SPECIALS AVAILABLE INCLUDE

Nominal zener voltages between the voltages shown and tighter voltage, for detailed information on price, availability and delivery, contact you nearest Tak Cheong representative.

3. ZENER VOLTAGE (Vz) MEASUREMENT

The zener voltage $(\dot{V_z})$ is tested under pulse condition. The measured V_z is guaranteed to be within specification with device junction in thermal equilibrium.

4. ZENER IMPEDANCE (Zz) DERIVATION

Zener impedance is derived from the 60-cycle ac voltage, which results when an ac current having an RMS value equal to 10% of the dc zener current (I_{zT}) is superimposed to I_{zT} .



Package Outline

Package Case Outline

DO-35

	DO-35				
DIM	Millin	neters	Inches		
	Min	Max	Min	Max	
Α	0.46	0.55	0.018	0.022	
В	3.05	5.08	0.120	0.200	
С	25.40	38.10	1.000	1.500	
D	1.53	2.28	0.060	0.090	

Notes:

- All dimensions are within JEDEC standard.
- DO35 polarity denoted by cathode band.

This datasheet presents technical data of Tak Cheong's Zener Diodes. Complete specifications for the individual devices are provided in the form of datasheets. A comprehensive Selector Guide is included to simplify the task of choosing the best set of components required for a specific application. For additional information, please visit our website http://www.takcheong.com.

Although information in this datasheet has been carefully checked, no responsibility for the inaccuracies can be assumed by Tak Cheong. Please consult your nearest Tak Cheong's sales office for further assistance.

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